

## UTILITY PATENT APPLICATION TRANSMITTAL UNDER 37 C.F.R.§1.53(b)

U.S. Patent and Trademark Office 2011 South Clark Place Officenmer Window, Mail Stop Patent Application Cristal Plaza Two, Lobby, Room 1B03 Artigton, VA 22202

Case Docket No.: INTEL-0057

Sir:

Transmitted herewith for filing is the patent application of Peter HAZUCHA, Gerhard SCHROM, Tanay KARNIK and Vivek DE

FOR: TIMIN CONV	G CIRCUIT FOR SEI ERTER	PARATE POSITIVI	E AND NEGATIVI	E EDGE PLACEME	NT IN A SWITCHING DC-DO
2.	ages of specification, clasts of FORMAL drawings of newly executed Doney (copy or original). Ity Claimed to Appln. Nesclosure is incorporated cant claims Small Entity mation Disclosure State eference.	o(s) filed herein by reference Status. \$1.136(a)(3).	of 8.	Assignment Papers for cover sheet, assignment Certified copy of  vo (2) return postcard Stamp & Return wit Prepaid Postcard-Stamp & Return Certurned with Unot Other:	nt & assignment fee).  s. h Courier.
CLAIMS AS I	ILED	4		100 P 100	
For	No. Filed		No. Extra	Rate	Fee
Total Claims	30	- 20	10	X \$18.00	\$180.00
Indep. Claims		- 3	2	X \$86.00	\$172.00
Multiple Dependent Claims (If applicable) X \$290.00					
BASIC FEE					\$770.00
TOTAL FILING FEE					\$1122.00
incorpora	of the prior application and by reference therein the specification by in This application is a content amount of \$1122.0	is considered as being a serting before the fire	ng part of the disclo  rst line the sentence: f Application Serial 1	sure of the accompan	n By Reference of the entire ying application and is hereby

Docket No.: INTEL-0057

- The Commissioner is hereby authorized to charge payment of following fees during the pendency of this application or credit any overpayment to Deposit Account No. 16-0607.
  - Any additional filing fees required under 37 C.F.R. 1.16.
  - Any patent application processing fees under 37 C.F.R. 1.17.
  - Any filing fees under 37 C.F.R. 1.16 for presentation of extra claims.

## Please direct all correspondence to Customer Number 34610

Respectfully Submitted, FLESHNER & KIM, LLP Attorneys for Intel Corporation

Mark Fleshner

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P.O. Box 221200 Chantilly, Virginia 20153-1200 703 766-3701 DYK:SWN/kdb Date: December 31, 2003

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Peter HAZUCHA et al.

Serial No.:

**NEW** 

: Group Art Unit

Filed:

December 31, 2003

Examiner:

For:

TIMING CIRCUIT FOR SEPARATE POSITIVE AND NEGATIVE EDGE

PLACEMENT IN A SWITCHING DC-DC CONVERTER

## AUTHORIZATION TO TREAT A REPLY AS INCORPORATING AN EXTENSION OF TIME UNDER 37 C.F.R. §1.136(a)(3)

U.S. Patent and Trademark Office 2011 South Clark Place Customer Window Crystal Plaza Two, Lobby, Room 1B03 Arlington, Virginia 22202

Sir:

The U.S. Patent and Trademark Office is hereby authorized to treat any concurrent or future reply that requires a petition for an extension of time under this paragraph for its timely submission, as incorporating a petition for extension of time for the appropriate length of time under 37 C.F.R. 1.136(a)(3). The U.S. Patent and Trademark Office is hereby authorized to charge all required extension of time fees to our Deposit Account No. 16-0607, if such fees are not otherwise provided for in such reply. A duplicate copy of this sheet is enclosed.

Respectfully submitted, FLESHNER & KIM, LLP Attorneys for Intel Corporation

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P.O. Box 221200 Chantilly, VA 20153-1200 703 766-3701MLF:SWN/kdb **Date: December 31, 2003**